## NSN 5962-01-508-3918

Digital Microcircuit - Page 1 of 1



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Operating Tempurature Range:
+0.0/+70.0 degrees celsius
Storage Tempurature Range:
-65.0/+150.0 degrees celsius
Features Provided:
W/preset and w/clear and w/complement
Output Logic Form:
Transistor-transistor logic
Design Function And Quantity:
1 flip-flop, d-type
Terminal Surface Treatment:
Solder
Product Name:
Dual positive-edge-triggered d flip-flops with preset, clear and complementary outputs
Voltage Rating And Type Per Characteristic:
7.0 volts input
Shelf Life:
N/a
Unit Of Measure:
Demilitarization:
Yes - demil/mli
Fiig:
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