## NSN 5962-01-509-5423

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Maximum Power Dissipation Rating:
500.0 milliwatts
Operating Tempurature Range:
-55.0/+125.0 degrees celsius
Storage Tempurature Range:
-65.0/+150.0 degrees celsius
Features Provided:
Monolithic and w/clear and w/preset
Inclosure Material:
Ceramic
Inclosure Configuration:
Leadless flat pack
Output Logic Form:
Complementary-metal oxide-semiconductor logic
Design Function And Quantity:
1 flip-flop, d-type
Terminal Surface Treatment:
Solder
Product Name:
Microcircuit, digital, advanced cmos, dual d-type flip-flop with preset and clear, ttl compatible inputs, monolithic silicon
Voltage Rating And Type Per Characteristic:
-0.5 volts input and 6.0 volts input
Special Features:
Generic p/n 54act74
Test Data Document:
96906-mil-std-883 standard (includes industry or association standards, individual manufactureer standards, etc.).
Terminal Type And Quantity:
20 leadless
Shelf Life:
N/a
Unit Of Measure:
Demilitarization:
No
Fiig:
A458a0