## NSN 5962-01-515-0577

Memory Microcircuit - Page 1 of 1



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| End Application:   |
|--|
| Test station r-iais 4920-01-495-9100, e/i f-16                     |
| Features Provided:   |
| Electrostatic sensitive  |
| Inclosure Configuration:   |
| Dual-in-line   |
| Memory Device Type:  |
| Ram  |
| Memory Capacity:   |
| 256mb  |
| Special Features:  |
| Consists of 2ea 128mb 266mhz double data rate sdram, necc, modules |
| Terminal Type And Quantity:  |
| 184 leadless   |
| Shelf Life:  |
| N/a  |
| Unit Of Measure:   |
|  |
| Demilitarization:  |
| Yes - demil/mli  |
| Fiig:  |
| A458a0   |
|  |
|  |