## NSN 5962-01-553-8654

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Pr∩	duc	t Na	me:

Programmable divide-by-n dual 4-bit binary/bcd down counter

## **Special Features:**

Speed-up circuitry for zero detection, each 4-bit counter can divide independently in bcd or binary mode, can be cascaded with mc14526b for frequencysynthesizer applications, all outputs are buffered, schmitt triggered clock conditioning

Shelf Life:

N/a

**Unit Of Measure:** 

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Demilitarization:

Yes - demil/mli

Fiig:

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