## NSN 5962-01-561-9502

Linear Microcircuit - Page 1 of 1



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## **End Application:**

COMP-UT-ER,-FLIG

## **Special Features:**

Package 28 ld cerdip. 64 k, 8k x 8 bit; byte alterable eeprom. Floatinggate cmos technology. Programmable nonvolatile memories. 5v only device, entire memory typically written in 0.25 seconds, features data polling and toggle bit polling, early end of write detection. 80ns access time, no external high voltages or vpp control circuits, self timed, no erase before write

Shelf Life:

N/a

Unit Of Measure:

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Demilitarization:

Yes - demil/mli

Fiig:

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