# NSN 5962-01-564-4652

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Between 1.013 inches and 1.040 inches

**Body Width:** 

Between 0.300 inches and 0.320 inches

**Body Height:** 

0.130 inches

#### **Operating Tempurature Range:**

-40.0/+85.0 degrees celsius

#### **End Application:**

Mark iii helicopters (sh-60b); panel id cass otps; cca, age (a1); mh-605/s cass otps: panel id pccn: p2j990

#### **Inclosure Material:**

**Plastic** 

#### **Inclosure Configuration:**

Dual-in-line

#### **Output Logic Form:**

Complementary-metal oxide-semiconductor logic

## **Design Function And Quantity:**

1 buffer, tri-state

# **Case Outline Source And Designator:**

Ms-001.0. Inch wide joint electron device engineering council

# **Current Rating Per Characteristic:**

1.00 microamperes reverse current, dc microamperes

#### **Product Name:**

Inavy: microcircuit; dwg: non-inverting octal 3-state buffer

# Voltage Rating And Type Per Characteristic:

4.5 volts total supply and 5.5 volts total supply

# **Capitance Rating Per Characteristic:**

15.00 input picofarads and 20.00 output picofarads

#### **Memory Capacity:**

Any acceptable

# **Special Features:**

Non-inverting buffer; has 2 active low enables, each enable independently controls 4 buffers

## **Supplementary Features:**

Designed to interface between ttl and nmos comp0nents and standard cmos devices; also as plug-in replacements for ls-ttl devices; can be used to reduce power consumption in existing designs

#### **Terminal Type And Quantity:**

20 pin

# Shelf Life:

N/a

# Unit Of Measure:

#### **Demilitarization:**

Yes - demil/mli

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