NSN 5962-01-575-5773

Memory Microcircuit - Page 1 of 1



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Operating Tempurature Range:

-40.0/+100.0 degrees celsius

Features Provided:

Bidirectional and cascadable and edge triggered and high performance and high speed and low power and programmable and w/clock

Inclosure Material:

Plastic

Inclosure Configuration:

Flat pack

Output Logic Form:

Complementary-metal oxide-semiconductor logic

Product Name:

Virtex ii field programmable gate array

Memory Device Type:

Prom

Special Features:

leee compliant; lead free

Terminal Type And Quantity:

575 pin

Shelf Life:

N/a

Unit Of Measure:

Demilitarization:

Yes - demil/mli

Fiig:

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