NSN 5962-01-578-1642

Integrated Circuit - Page 1 of 1



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Special Features:

Architectural advantage-single power supply operation -2.7 to 3.6 volt read and write operations for battery powered applications; manufactured on 200nm process technology -compatible with 0.32 um and 230 nm am 29lv160 devices, flexible sector arhitecture, unlock bypass program command, tope or bottom command, top or bottom boot block configurations available, embedded algorithms, combaatiblity with jedec standards, high performance, ultra low power consumption

Ch.	_lf	Life:	
OII	eII	LIIE.	

N/a

Unit Of Measure:

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Demilitarization:

Yes - demil/mli