NSN 5962-01-585-2214

Memory Microcircuit - Page 1 of 1



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End Application:
Sdram high density
Inclosure Configuration:
Dual-in-line Dual-in-line
Output Logic Form:
Complementary-metal oxide-semiconductor logic
Product Name:
184 pin ddr266 1u registered dimm 1gb with 128mx4 cl2.5
Special Features:
Item is 128m x 72bits double data rate sdram high density for ddr266; item consists of 18 pcs cmos 128mx4 bits double dta rate sdrams in
60 ball fbga packages, 2pcs drive ic's for input control signal, 1pc pll, and a 2048 bits serial eeprom on a 184-pin printed circuit board
Shelf Life:
N/a
Unit Of Measure:
Demilitarization:
Yes - demil/mli
Fiig:
A458a0