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General Description:

Transistor-transistor output logic form; optically coupled gate single design function and quacity; input circuit pattern 3 input; operating temperature range 0.00 to 70.00 degrees c; terminalquantity type 8; terminal surface treatment tin plate; inclosure configuration dual-in-line; inclosure material ceramic and glass; 0.390 in. lg; 0.165 in. h; 0.260 in. w; power dissipation rating 40.00 milliwatts

Shelf Life:

N/a

Unit Of Measure:

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Demilitarization:

Yes - demil/mli

Fig:

A23900