NSN 6625-01-205-5224

Logic Analyzer System - Page 1 of 1



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General Description:

Analyze digital signals in the time asynchronous and data synchronous domain up to 50 mhz, 54 display input channels, dual timebase triggering acquisition and display, 20 ns 50 mhz to 1 s 1 hz internal clock, 14 separate trigger levels for each input channel, trigger delay of 1 to 9, 999 system clocks or event occurances, fixed ttl, ecland tpg and variable logic threshold, internal 512 bit by 54 channel, 48 to 440 hz, single phase, o/a dimensions 19.600 in. Lg, 14.500 in. W, 7.800 in. H

N/a	
Unit Of Measure:	
Demilitarization:	

Shelf Life:

No