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**Special Features:**

Data bits, architecture, chf /chip family-memory, 65k0-input/output, 9 dev-basic micro instr, 72-no. Addr mode, 7-interrupt level no. And type, v/vectored/-instruct time-type , 2.0u min.-no. General purpose reg, 6-stack level-m/multiple/-other capabilities, dma and multi-processor arith., dec., bin., dma uses 8080 mpu-instr set ref. No., is-6 system dwg.-is6-mfr code, mota

**Shelf Life:**

N/a

**Unit Of Measure:**

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**Demilitarization:**

Yes - demil/mli

**Fig:**

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