NSN 7025-01-130-1320

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Special Features:

cycle time, full cycle 3.35 microseconds; split cycle 3.5 microseconds; read access time is 2.1 microseconds; modes of operation are clear-write, read-restore, read-modify-write; read-pause-restore; random access memory addressing; logic is ttl integrated circuit series; input levels hig (false) 2.5 v to 5.5 v, low(true) 0.0 v to 0.6v; output levels high 2.4 to 5.0 v; low 0.0v to 0.6 v; operating power ac, 95 to 135 v, single phase, 47-63 hz, 20 amps at 115 v ac; 0/a dims. 22.75 in. L x 19 in. W x 26.5 in. H. 150 lbs., rack mtd

Test Data Document:

98230-0n239314 drawing (this is the basic governing drawing, such as a contractor drawing, original equipment manufacturer drawing, etc.; excludes any specification, standard or other document that may be referenced in a basic governing drawing) Shelf Life:

N/a

Unit Of Measure:

Demilitarization:

Yes - demil/mli

Fiig:

A505e0