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**End Application:**

An/arc 222 sincgars

**Output Logic Form:**

Transistor-transistor logic

**Input Circuit Pattern:**

4 input

**Memory Capacity:**

128kx5

**Special Features:**

16-channel ttl stimulus/response module with each channel incorporating 5 128k memories. Memories nomenclated output, tri-state, expect, mask, and record. Data rates to 25mhz. 2 memories per output channel, 3 per input channel. 5 volts power supply requirement.

**Shelf Life:**

N/a

**Unit Of Measure:**

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**Demilitarization:**

Yes - demil/mli

**Fig:**

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